

NXDB 500-SYS

netX 500 System Development Board



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Description

The System Development Board is the base for various hardware and software developments around the netX.

Different types of memory and interfaces, Touch LCD Display, as well as switches and LEDs as digital inputs and outputs allow putting almost all functions into operation and to access signals on pin connectors.

The host interface can be tested with a DIMM PC or in the Extension Bus mode via inserted DIMM memory boards (ordered separately).

Technical Data

- netX 500 with Master License
- 32 MByte Flash (2x 8Mx16 Bit)
- 64 MByte SDRAM (2x 16Mx16 Bit)
- 256 KByte SRAM (1x 128Kx16 Bit)
- 32 KByte NVRAM (1x 32Kx8 Bit)
- 512 KByte Serial SPI Flash (1x 4Mx1 Bit)
- 240x320 Pixel 3,5" Color TFT LCD with Touch and Backlight
- Two Ethernet-Ports with Switch and Hub Functionality
- Fieldbus Interface for AS-Interface (Master only), CANopen, CC-Link, DeviceNet, InterBus (Master only) and PROFIBUS
- USB 1.1 Host / Device
- Multi Media Card Socket
- 3 UARTs with RS232C-Interface
- IrDA
- JTAG- and ETM Interface
- CPU Signals Accessible via Grouped Pin Connectors
- DIMM Socket for DIMM PCs or Memory Card



We recommend the Hitex Tantino JTAG emulator for programming and start-up. Online recording of the command executions is possible via the ETM interface as offered by the Hitex-Tanto.

See <http://www.hitex.com/products.html>

Product Overview

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Internet Shop

www.hilscher-shop.com

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